Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	microcontroller near5 interrupt same delay and wait near5 (routine protocal)	US-PGPUB; USPAT	OR	OFF	2005/05/04 08:50
L2	1	microcontroller near5 interrupt same delay and wait with (routine protocal)	US-PGPUB; USPAT	OR	OFF	2005/05/04 08:50
L3	3	microcontroller near5 interrupt same delay and wait same (routine protocal)	US-PGPUB; USPAT	OR	OFF	2005/05/04 08:50
L4	2475	(250/214.1).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/04 09:13
L5	1118	(398/202-214).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/04 09:14
L6	191	5 and compar$4 with reference	US-PGPUB; USPAT	OR	OFF	2005/05/04 09:14
S1	3660	(250/214r).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/02 16:03
S2	170	S1 and compa$6 with reference	US-PGPUB; USPAT	OR	OFF	2005/05/02 16:22
S3	88	S1 and compa$6 with reference and bias$4	US-PGPUB; USPAT	OR	OFF	2005/05/02 16:24
S4	17	S1 and compa$6 with reference and bias$4 and avalanche	US-PGPUB; USPAT	OR	OFF	2005/05/03 13:52
S5	17	compa$6 with reference and bias$4 and avalanche and latch with (flip-flop flip adj flop)	US-PGPUB; USPAT	OR	OFF	2005/05/03 13:58
S6	3	compa$6 with reference and bias$4 same avalanche and latch with (flip-flop flip adj flop)	US-PGPUB; USPAT	OR	OFF	2005/05/03 14:25
S7	218	(250/214ag).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/03 14:30
S8	2665302	compa$6 reference$2 and bias$4 same avalanche and overload$2	US-PGPUB; USPAT	OR	OFF	2005/05/03 14:31
S9	25	compa$6 with reference$2 and bias$4 same avalanche and overload$2	US-PGPUB; USPAT	OR	OFF	2005/05/03 14:44
S10	4	("5467242").URPN.	USPAT	OR	OFF	2005/05/03 14:37
S11	5	("3743855" | "4525765" | "4551779" | "4893212" | "5200879").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/03 14:42

S12	52	(361/91.4).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/03 14:42
S13	32	compa$6 same reference$2 and bias$4 same avalanche and overload$2	US-PGPUB; USPAT	OR	OFF	2005/05/03 14:46
S14	7	S13 not S9	US-PGPUB; USPAT	OR	OFF	2005/05/03 14:45
S15	4	compa$6 same reference$2 and bias$4 same avalanche same overload$2	US-PGPUB; USPAT	OR	OFF	2005/05/03 14:49
S16	6621	compar$5 same bias$4 with (detector$2 diode$2)	US-PGPUB; USPAT	OR	OFF	2005/05/03 14:50
S17	178	compar$5 same bias$4 with (detector$2 diode$2) and overload same protect$5	US-PGPUB; USPAT	OR	OFF	2005/05/03 14:51
S18	0	compar$5 same bias$4 with (avalanche near5 (detector$2 diode$2)) and overload same protect$5	US-PGPUB; USPAT	OR	OFF	2005/05/03 16:17
S19	0	compar$5 same bias$4 with (avalanche and (detector$2 diode$2)) and overload same protect$5	US-PGPUB; USPAT	OR	OFF	2005/05/03 14:52
S20	178	compar$5 same bias$4 with (detector$2 diode$2) and overload same protect$5	US-PGPUB; USPAT	OR	OFF	2005/05/03 14:52
S21	2475	(250/214.1).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/03 16:17
S22	0	S21 and overload same compar$5 near5 reference$2 near2 voltage	US-PGPUB; USPAT	OR	OFF	2005/05/03 16:18
S23	0	S21 and overload same compar$5 near5 reference$2	US-PGPUB; USPAT	OR	OFF	2005/05/03 16:18
S24	18	S21 and overload same compar$5 withreference$2	US-PGPUB; USPAT	OR	OFF	2005/05/03 16:18
S25	0	S21 and overload same compar$5 with reference$2	US-PGPUB; USPAT	OR	OFF	2005/05/03 16:18
S26	18	S21 and (overload over adj load) same compar$5 withreference$2	US-PGPUB; USPAT	OR	OFF	2005/05/03 16:19
S27	0	S21 and (overload over adj load) same compar$5 with reference$2	US-PGPUB; USPAT	OR	OFF	2005/05/03 16:19
S28	0	S21 and (overload over adj load) same compar$5 same reference$2	US-PGPUB; USPAT	OR	OFF	2005/05/03 16:19
S29	0	S21 and (overload over adj load) and compar$5 with reference$2	US-PGPUB; USPAT	OR	OFF	2005/05/03 16:19

S30	0	S21 and (overload over adj load) and compar$5 same reference$2	US-PGPUB; USPAT	OR	OFF	2005/05/03 16:19
S31	2	S21 and (overload over adj load)	US-PGPUB; USPAT	OR	OFF	2005/05/03 17:34
S32	6	"817578"	US-PGPUB; USPAT	OR	OFF	2005/05/03 17:44
S33	38559	microcontroller	US-PGPUB; USPAT	OR	OFF	2005/05/03 17:44
S34	397	microcontroller near5 latch	US-PGPUB; USPAT	OR	OFF	2005/05/03 17:44
S35	74	microcontroller near5 interrupt same delay	US-PGPUB; USPAT	OR	OFF	2005/05/04 09:13